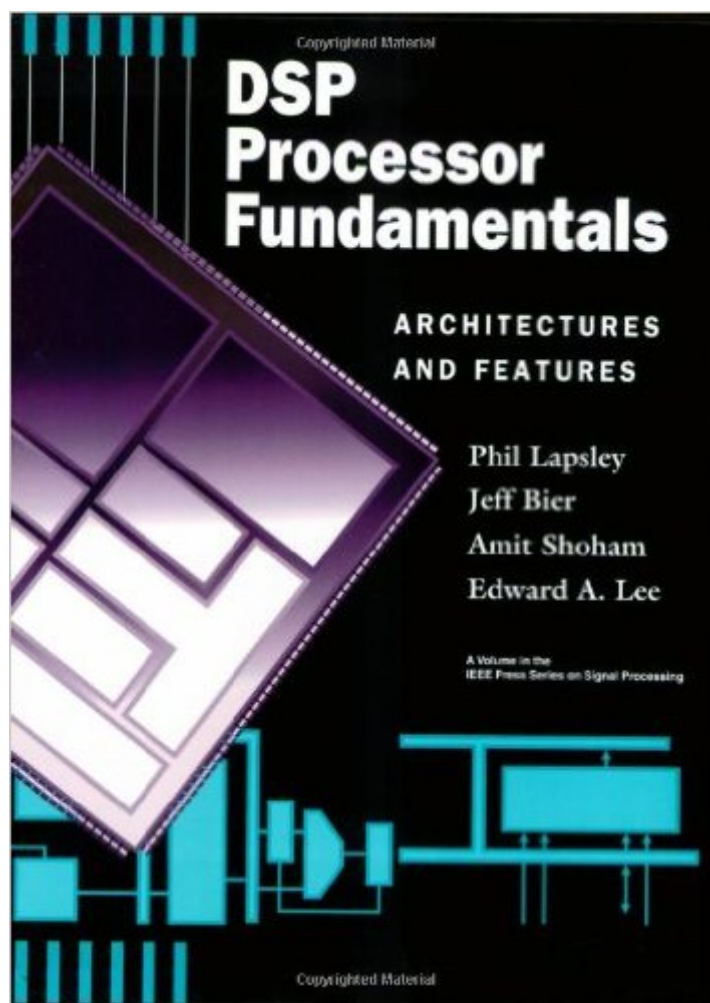


The book was found

DSP Processor Fundamentals: Architectures And Features



Synopsis

This cutting-edge, practical guide brings you an independent, comprehensive introduction to DSP processor technology. A thorough tutorial and overview of DSP architectures, this book incorporates a broad range of today's product offerings in examples that illustrate DSP features and capabilities. This book is especially useful to electronic systems designers, processor architects, engineering managers, and product planners.

Book Information

Paperback: 224 pages

Publisher: Wiley-IEEE Press; 1 edition (February 7, 1997)

Language: English

ISBN-10: 0780334051

ISBN-13: 978-0780334052

Product Dimensions: 6.8 x 0.4 x 9.2 inches

Shipping Weight: 15.5 ounces (View shipping rates and policies)

Average Customer Review: 4.5 out of 5 starsÂ Â See all reviewsÂ (2 customer reviews)

Best Sellers Rank: #1,877,434 in Books (See Top 100 in Books) #67 inÂ Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design > DSPs #507 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Digital Design #1218 inÂ Books > Computers & Technology > Networking & Cloud Computing > Data in the Enterprise

Customer Reviews

BDTI, employer of this book's authors, is the world's foremost independent authority on the analysis of DSP processor designs. This book details many of the aspects of processor design that yield a processor that performs well on DSP applications. As a colleague of mine once remarked, "This book gives away all the secrets."The writing style of this book is brief and to the point, but complete. It uses many commercial DSP processors as examples but does not focus on any particular one in complete detail.This book will be most useful to hardware designers of DSP processors. It is not a reference for DSP algorithm or software developers.

The Pipelined concepts are very good , with examples of different processors and its arcitechtures the material provided with examples is very good.

[Download to continue reading...](#)

DSP Processor Fundamentals: Architectures and Features Embedded DSP Processor Design, :
Application Specific Instruction Set Processors (Systems on Silicon) DSP without math: A brief
introduction to DSP The Art of DSP: An innovative introduction to DSP Fundamentals of Neural
Networks: Architectures, Algorithms And Applications Modern Processor Design: Fundamentals of
Superscalar Processors MODERN PROCESSOR DESIGN: Fundamentals of Superscalar
Processors, Beta Edition Web Services, Service-Oriented Architectures, and Cloud Computing: The
Savvy Manager's Guide (The Savvy Manager's Guides) UNIX Systems for Modern Architectures:
Symmetric Multiprocessing and Caching for Kernel Programmers Parallel Programming with
Microsoft Visual C++: Design Patterns for Decomposition and Coordination on Multicore
Architectures (Patterns & Practices) VLSI Test Principles and Architectures: Design for Testability
(The Morgan Kaufmann Series in Systems on Silicon) Documenting Software Architectures: Views
and Beyond (2nd Edition) Internet Routing Architectures (2nd Edition) Business Process
Management: Concepts, Languages, Architectures Optimizing Compilers for Modern Architectures:
A Dependence-based Approach Fundamentals of Nursing: Human Health and Function (Craven,
Fundamentals of Nursing: Human Health and Function Craven, Fundamentals of Nurs) How
Computers Work: Processor And Main Memory (Second Edition) Embedded SoPC Design with
Nios II Processor and Verilog Examples Cook Book: 20 Easy Recipes for Busy Parents: The Best:
Fast and Easy, Homemade Food Using the Manual Food Processor Master Slicer Digital Signal
Processing Applications With Motorola's DSP56002 Processor

[Dmca](#)